



**ISIE 2007 – IEEE International Symposium on Industrial Electronics**  
June 4-7, 2007, Centro Cultural & Centro Social Caixanova, Vigo, Spain

**Special Session on**  
**Electronics System On Chip (ESOC)**

**organized by**

Vito Nardi (nardi@unicas.it) Manus Henry (manus.henry@eng.ox.ac.uk)

**Call for Papers**

Topics of interest include, but are not limited to:

- System on chip design
- Industrial control systems based on FPGA/SOC
- FPGAs/PLDs application in power electronics

**Submission procedure and deadlines: The same as for regular papers.**

All the instructions for paper submission are included in the conference website:

<http://www.dte.uvigo.es/isie2007/>